

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Thomas L. Davis
Filing Date: September 16, 2003
For: SYSTEM AND METHOD FOR FREQUENCY TRANSLATION
WITH HARMONIC SUPPRESSION USING MIXER STAGES

Mail Stop Patent Application
Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Applicant respectfully requests, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. No representation is made that a search has been made, that the references are material to the patentability of the present application, or that the references qualify as prior art.

Respectfully submitted,

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PTO-1449	Application No. Unknown	Applicant(s) Thomas L. Davis	
	Docket Number 073897.0191	Group Art Unit Unknown	Filing Date Sept. 15, 2003

**Information Disclosure Citation
In an Application**

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						
	L						
	M						
	N						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	O							
	P							

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	Q	Watanabe et al., "Integrated Mixer Design," Motorola Inc., Semiconductor Products Sector, pp. 1-4	
	R	Leong et al., "Monolithic RF Active Mixer Design," IEEE Transactions on Circuits and Systems—II: Analog and Digital Signal Processing, Vol. 46, No. 3, pp. 231-239	March 1999
	S	Weldon et al., "10.4: A 1.75GHz Highly-Integrated Narrow-Band CMOS Transmitter with Harmonic-Rejection Mixers," ISSCC 2001 Visuals Supplement/IEEE, pp. 126-127 and 420	2001
	T	Weldon et al., "10.4: A 1.75GHz Highly-Integrated Narrow-Band CMOS Transmitter with Harmonic-Rejection Mixers," IEEE International Solid-State Circuits Conference, pp. 160-161 and 442	2001
	U	Weldon et al., "A 1.75GHz Highly Integrated Narrow-Band CMOS Transmitter with Harmonic-Rejection Mixers," IEEE Journal of Solid-State Circuits, Vol. 36, No. 12, pp. 2003-2015	December 2001

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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DAL01:756667.1